

## REMARKS

In response to the Office Action mailed November 22, 2004, reconsideration and allowance are respectfully requested.

The Examiner rejects all the pending claims as anticipated by a newly cited prior-art reference and has made the Action final. Applicants respectfully request reconsideration of the rejection's finality. As the Examiner concedes, the Action is based on "new ground(s) of rejection." (11/22/04 Office Action at 2.) Applicants therefore request that the finality of the November 22, 2004 Action be withdrawn. Alternatively, Applicants request that the present amendments be admitted after final rejection, because the newly cited reference provides good and sufficient reasons why the present amendments and remarks are necessary and were not earlier presented. (*See* 37 C.F.R. § 1.116(c).)

The rejection of claims 1-4 under § 102 as being anticipated by Brewer (U.S. Patent No. 6,677,799) is traversed.

The present invention concerns a power supply circuit in which both the output circuit (23) and the current generation circuit (124) are controlled by a voltage delayed by a delay circuit (22).

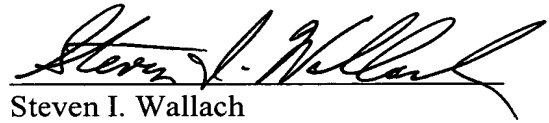
In contrast, the Brewer reference relates not to a power supply circuit, but to a circuit of operational amplifiers. Thus the op-amp circuit disclosed in Brewer has a completely different configuration from the power supply circuit of the present invention. In particular, Brewer discloses an op-amp circuit that is configured to add certain processing to an input signal that then generates an output signal. Brewer nowhere discloses delaying a power signal that is to be supplied to a load, as taught in the present invention.

Moreover, in a power supply circuit that embodies the present invention, the current generation circuit (124) generates a current according to the voltage that is delayed by the

delay circuit (22) and supplies the generated current to the output circuit (23) as a drive current. This feature is also nowhere disclosed in the Brewer reference. To clarify this feature in the present invention, claim 1 is now amended to recite "a current generation circuit generating a current according to the input constant voltage delayed by said delay circuit and supplying the generated current to said output circuit as a drive current."

In view of the foregoing, the Brewer reference does not anticipate either independent claim 1 or its dependent claims, claims 2-4. This application is therefore believed to be in condition for allowance, and a Notice to that effect is respectfully solicited.

Respectfully submitted,

A handwritten signature in black ink, appearing to read "Steven I. Wallach", written over a horizontal line.

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